

Docket No. 210067US2

RESPONSE UNDER 37 CFR 1.116-  
EXPEDITED PROCEDURE EXAMINING  
GROUP 2819

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Hidemasa ZAMA, et al.

SERIAL NO: 09/883,959

GAU: 2819

RCE FILED: February 28, 2003

EXAMINER: TAN, V.

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT, LOGIC OPERATION CIRCUIT, AND FLIP FLOP

REQUEST FOR EXTENSION OF TIME  
UNDER 37 C.F.R. 1.136

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

SIR:

It is hereby requested that a **one** month extension of time be granted to January 9, 2004 for

- ☒ filing a response to the Official Action dated: September 9, 2003
- ☐ responding to the requirements in the Notice of Allowability dated:
- ☐ filing the Formal Drawings. The Issue Fee due has been timely filed.
- ☐ responding to the Notice to File Missing Parts of Application dated:
- ☐ filing a Notice of Appeal. A timely response to the final rejection, due has been filed.
- ☐ filing an Appeal Brief. A Notice of Appeal was filed on:
- ☐ Applicant claims small entity status. See 37 CFR 1.27. Therefore, the fee amount shown below is reduced by one-half.

The required fee of \$110.00 is enclosed herewith by check and any further charges may be made against the Attorney of Record's Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

*W. Todd Baker*

Eckhard H. Kuesters

Registration No. 28,870

Customer Number

**22850**

Tel. (703) 413-3000  
Fax. (703) 413-2220  
(OSMMN 05/03)

I:\ATTY\Wtb\0039\210067US\1-Mo EOT 8 JAN 04.DOC

W. Todd Baker

Registration No. 45,265

01/12/2004 CNGUYEN 00000131 09883959

01 FC:1251

110.00 OP

RECEIVED  
JAN 13 2004  
TECHNOLOGY CENTER 2800